Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.045”**

**ANODE**

**.025 x .025”**

**.045”**

**Top Material: Al**

**Backside Material: TiNiAg**

**Bond Pad Size: .025 X .025”**

**Backside Potential: CATHODE**

**Mask Ref:**

**APPROVED BY: DK DIE SIZE .045” X .045” DATE: 10/21/21**

**MFG: MICROSEMI THICKNESS .011” P/N: 1N4003**

**DG 10.1.2**

#### Rev B, 7/1